



ALLUMINA LIGHTING SOLUTIONS

makes lighter to brighter

Ref :

Date : 15/02/2017

TO WHOM SO EVER IT MAY CONCERN

This is to certify that the following short term certificate courses of NIELIT containing the syllabus as mentioned below are considered for employment positions in the **VLSI /IP Design Engineer, Electronic Design Engineer, RTL Engineer, FPGA Design Engineer** as per market demand of the Electronic Industry / Organization:

1. Course Name: Certificate Course in VLSI Design

Syllabus:

Combinational Circuit Design, Sequential Circuit Design, Design of controller and Data path units, State Machines, Controller Design using FSMs & ASMs, Design Examples & Case Studies.
Introduction to Verilog HDL & Hierarchical Modeling Concepts, Modeling methods, Design Verification using Test benches, Introduction to Programmable Logic and FPGAs, Architecture of popular Xilinx and Altera FPGAs, FPGA Design Flow, Implementation Details, Advanced FPGA Design tips, Logic Synthesis for FPGA

2. Course Name: PG Diploma in VLSI & Embedded Hardware Design

Syllabus:

Advanced Digital Design, VHDL: Language and Coding for Synthesis, Verilog HDL: Language and Coding for Synthesis, RTL Verification (System Verilog, UVM), FPGA Design Methodology and Prototyping, CMOS Logic & Physical Design, Embedded Controller Based Product Design

3. Course Name: PG Diploma in Electronic System Design and Manufacturing

Syllabus:

Fundamentals of Electronics, Electronics System Packaging and Manufacturing, Industrial Electronic Product Design, Processor based System Design, Embedded Software Development; FPGA based System Design, Communication interface of Electronics Products, Internet of Things (IoT)

We as a manpower recruiting agency/Manufacturing/Design Company feel that the above courses of NIELIT will make the candidates suitable for employment opportunities in the design / services/manufacturing companies for the above stated job positions.

For ALLUMINA ELECTRONICS

Rashed.E.K

Managing Director

Proprietor



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This is to certify that the following short term certificate courses of NIELIT containing the syllabus as mentioned below are considered for employment positions in the Test Engineer, Verification Engineer, Physical Verification Engineer as per market demand of the Electronic Industry / Organization:

1. Course Name: Certificate Program System Verilog and UVM

Syllabus:

SystemVerilog modeling concepts, Data types, declarations, syntax rules, Procedural blocks and assignments, Programming statements, Operators and operation rules, Compound data types and packages, SystemVerilog interface ports, Verification constructs and testbench interfaces, Verification timing using clocking blocks, Object Oriented testbenches, Dynamic arrays and scoreboards, Constrained random stimulus generation, Functional coverage, Synchronization (events, mailboxes, semaphores), Assertions overview

2. Course Name: Advanced Diploma –VLSI Physical Design Engineer

Syllabus:

Introduction to IC Design Flow, Basic CMOS Digital IC Design, Processing and Layout, System Design and Design Methods, Analog IC Design.

We as a Manufacturing/Design Company feel that the above courses of NIELIT will make the candidates suitable for employment opportunities in the design / manufacturing.

For ALLUMINA ELECTRONICS

Rashed.E.K

Managing Director

Proprietor